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PATENT

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re application of : FRANCISCO JAVIER GUERRERO MERCADO  
Serial No. : 10/619,169  
Filed : July 14, 2003  
For : LOW POWER COMPARATOR WITH FAST PROPAGATION  
DELAY  
Group : 2811  
Examiner : (Not Yet Assigned)

**MAIL STOP MISSING PARTS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

**PRELIMINARY AMENDMENT**

Prior to examination, please amend the above-identified application as follows:

**AMENDMENTS TO THE SPECIFICATION:**

Please replace paragraph [0002] of the specification with the following:

As illustrated in the equivalent circuit diagram depicted in FIGURE 5, integrated circuit comparators typically include: a bias system generating a defined current bias to each transistor; an input differential pair--either complementary metal oxide semiconductor (CMOS) or bipolar junction transistors--that, for a given